

## Response under 37 C.F.R. § 1.116 Expedited Procedure Examining Group 2800

PATENT ATTORNEY DOCKET NO. 052593-5003

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Noriaki HIRAGA	) Confirmation No. 7356
Application No.: 09/625,643	) Group Art Unit: 2836
Filed: July 25, 2000	) Examiner: Z. Kitov
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH ENHANCED RESISTANCE TO ELECTROSTATIC	) Mail Stop AF )
BREAKDOWN	

Commissioner for Patents
U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, **Mail Stop AF**Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202



## **AMENDMENT UNDER 37 C.F.R. § 1.116**

In response to the Office Action dated September 5, 2003 (Paper No. 10), the period for response to which extends through December 5, 2003, entry of the following amendment is respectfully requested to place the application in clear condition for allowance or, alternatively, in better form for appeal.